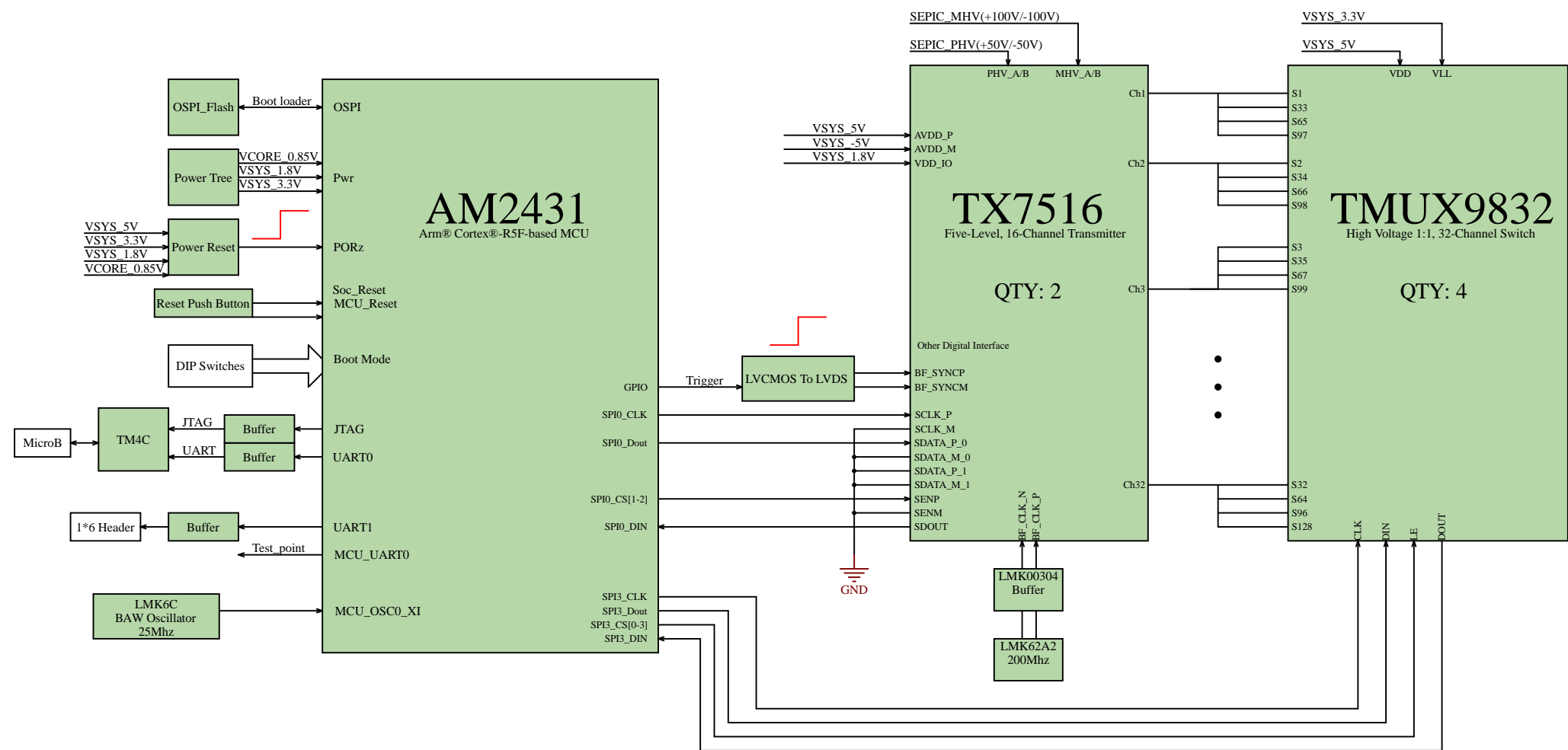
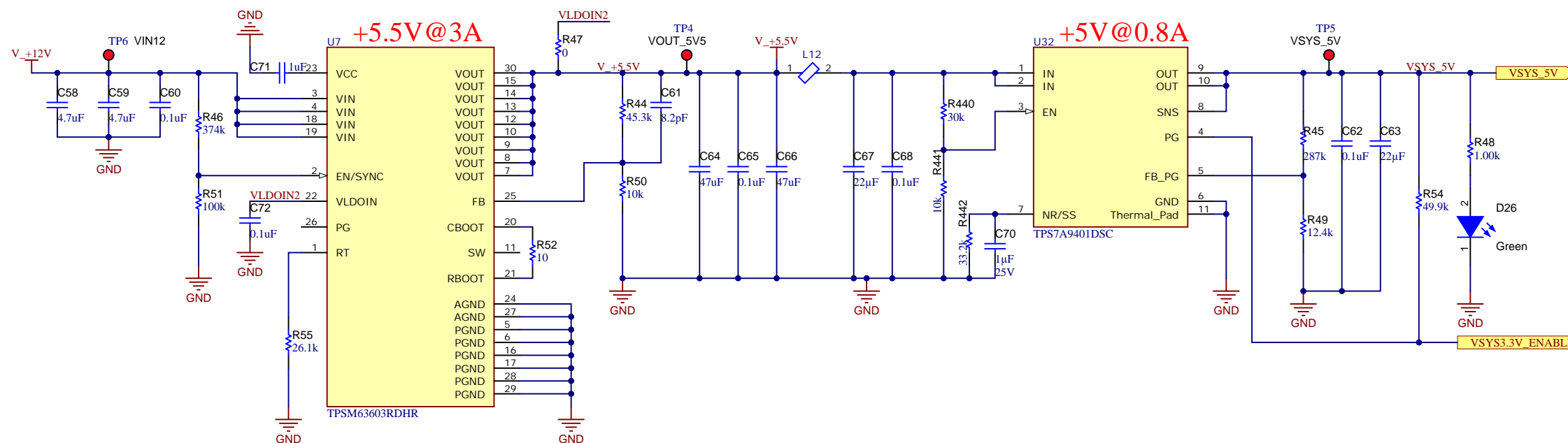
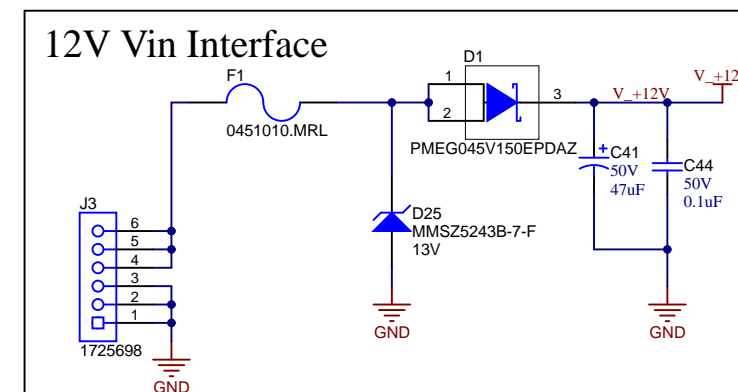
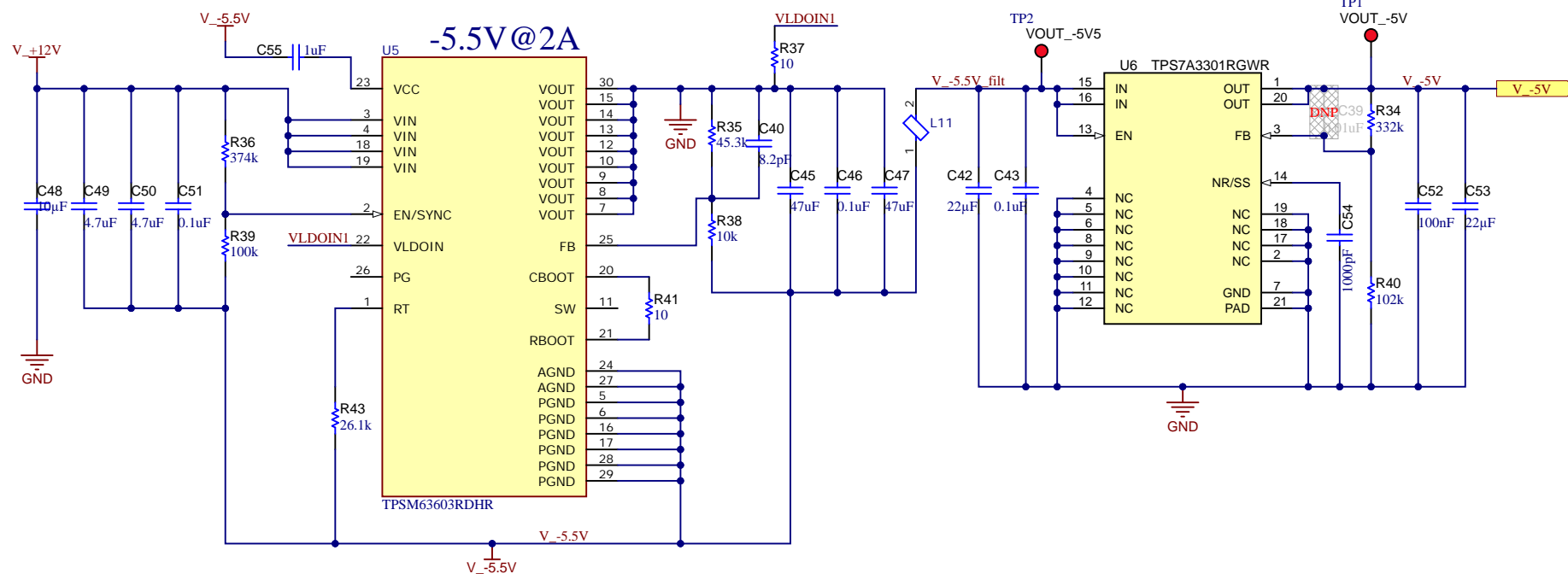


System Block Diagram

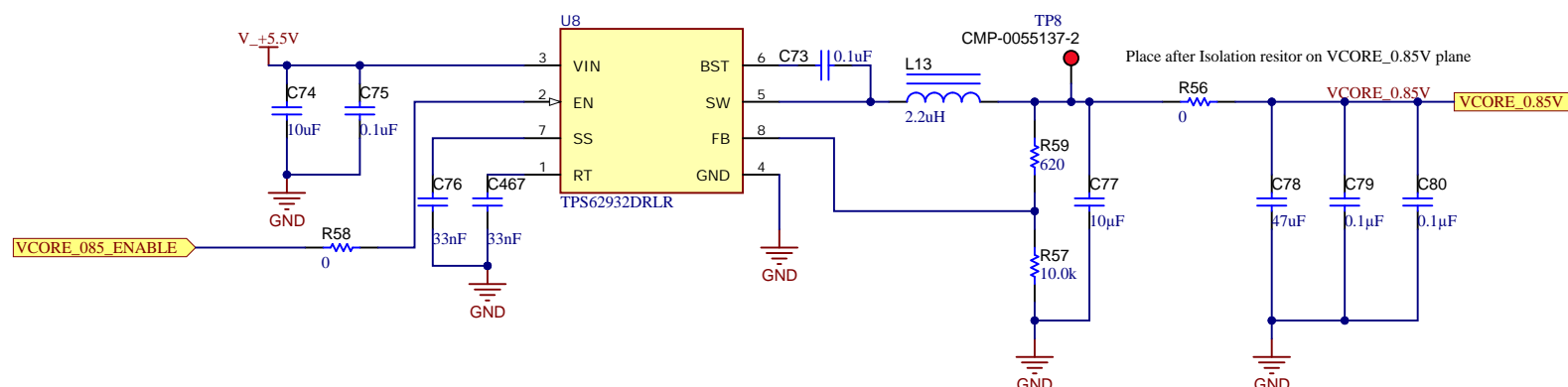


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Number: TIDA-010256	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 1 of 18
Drawn By: Jason Ding	File: 1_SystemDiagram_TIDA010256.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	



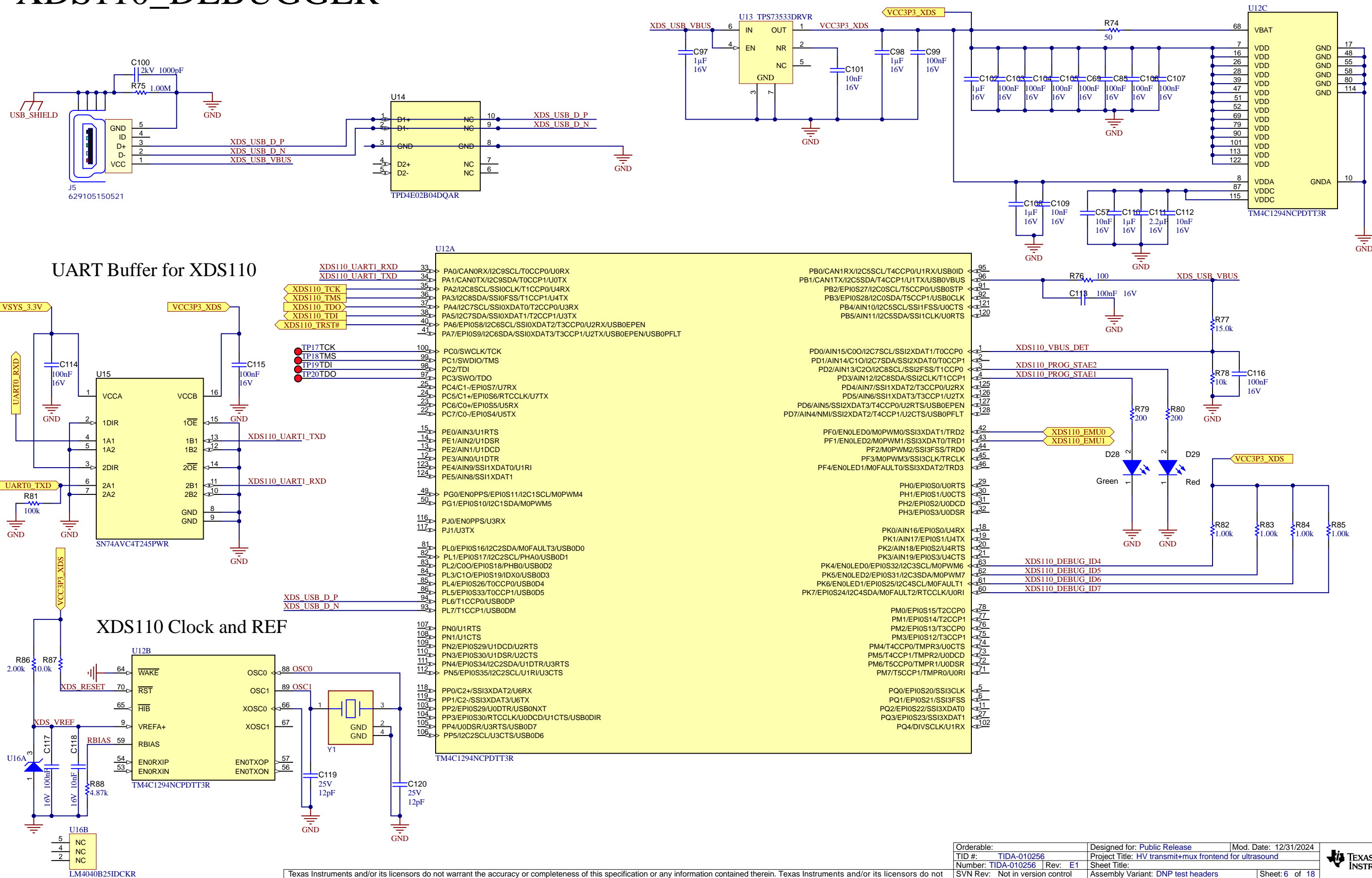
+0.85V@2A, Fsw=500khz



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TID #: TIDA-010256	Project Title: HV transmit+mux frontend for ultrasound	
Number: TIDA-010256	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 4 of 18
Drawn By:	File: 12_LowVoltage_PowerSupplies01.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	

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XDS110_DEBUGGER

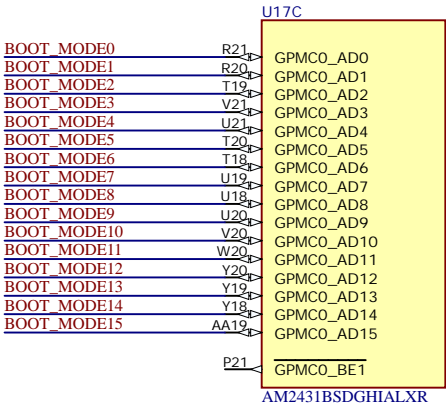
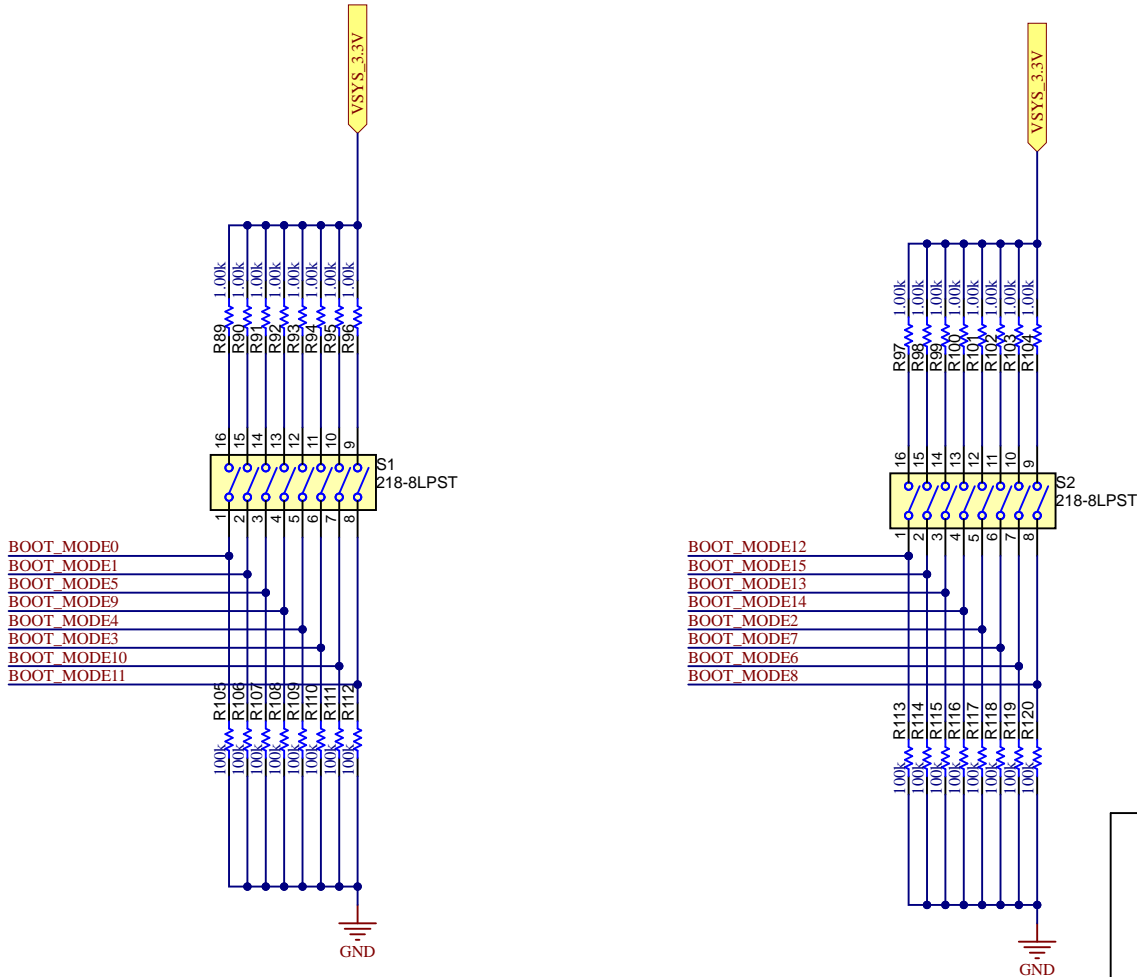


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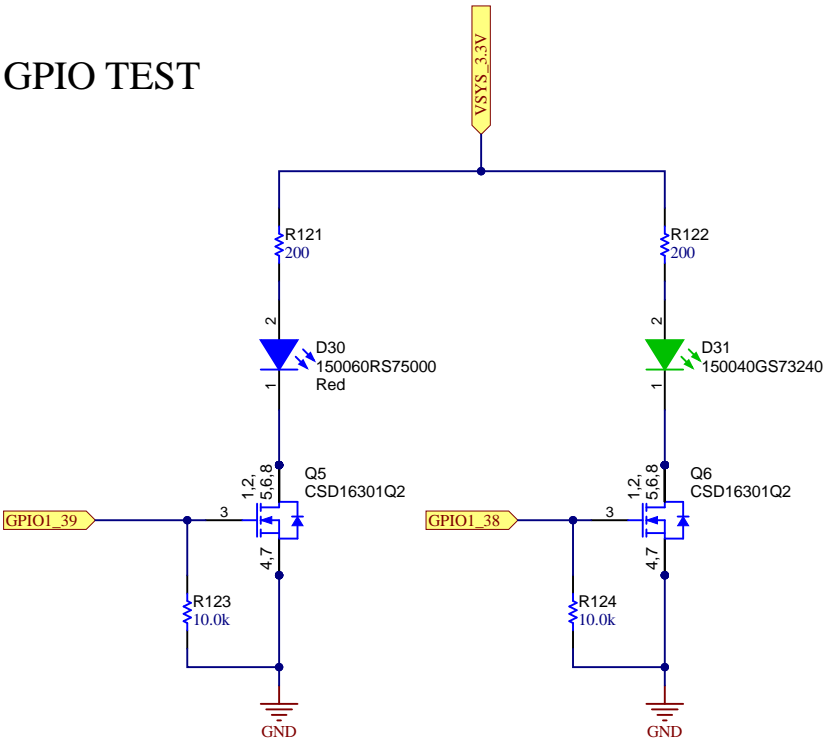
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SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 6 of 18
Drawn By:	File: 14_XDS110_Debugger.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	

AM243x_BOOTMODE

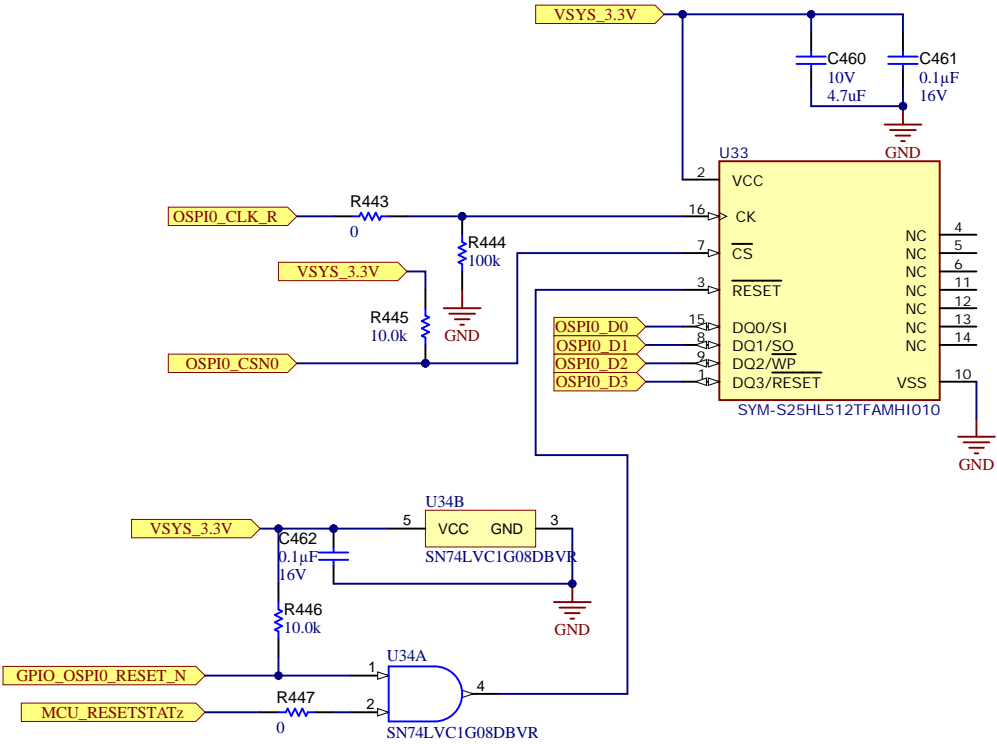
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GPIO TEST



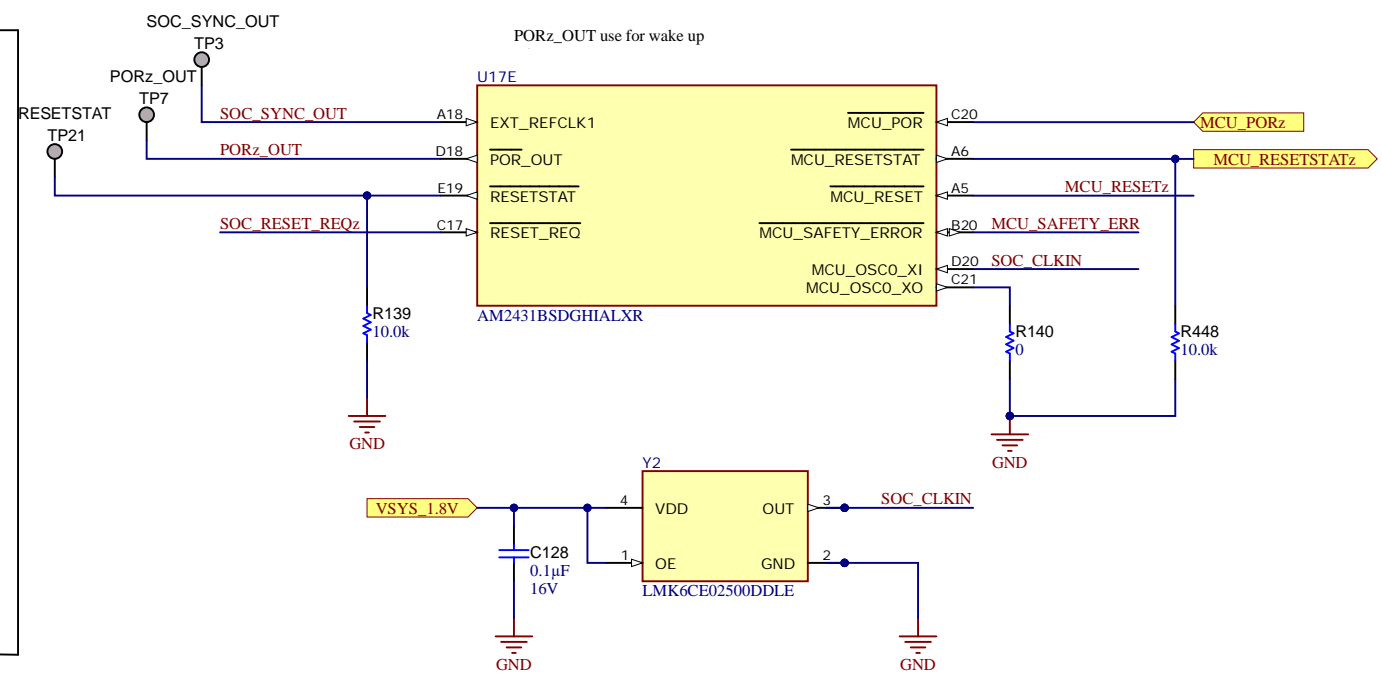
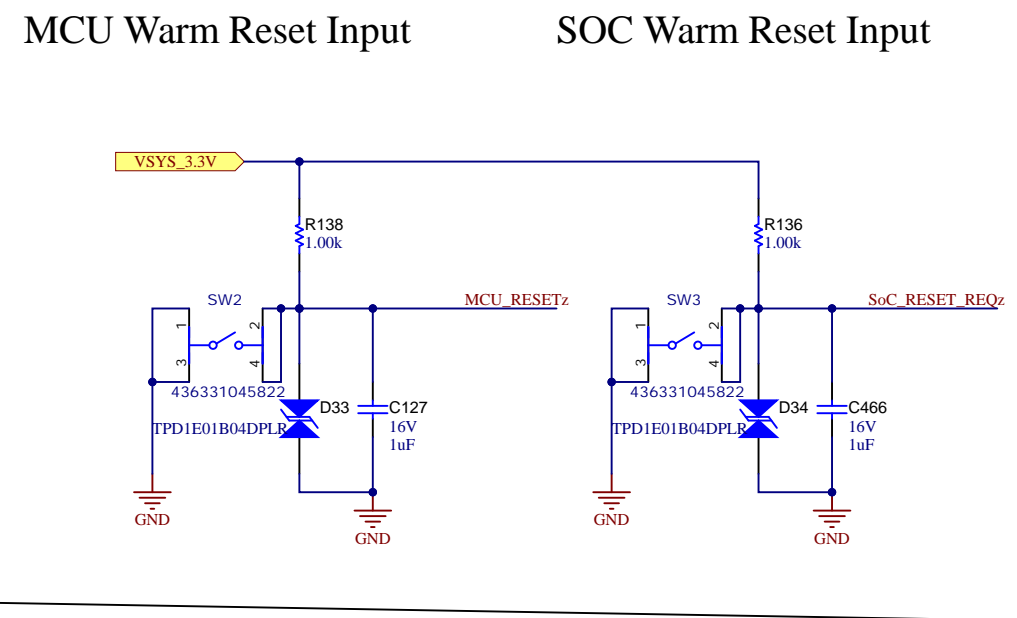
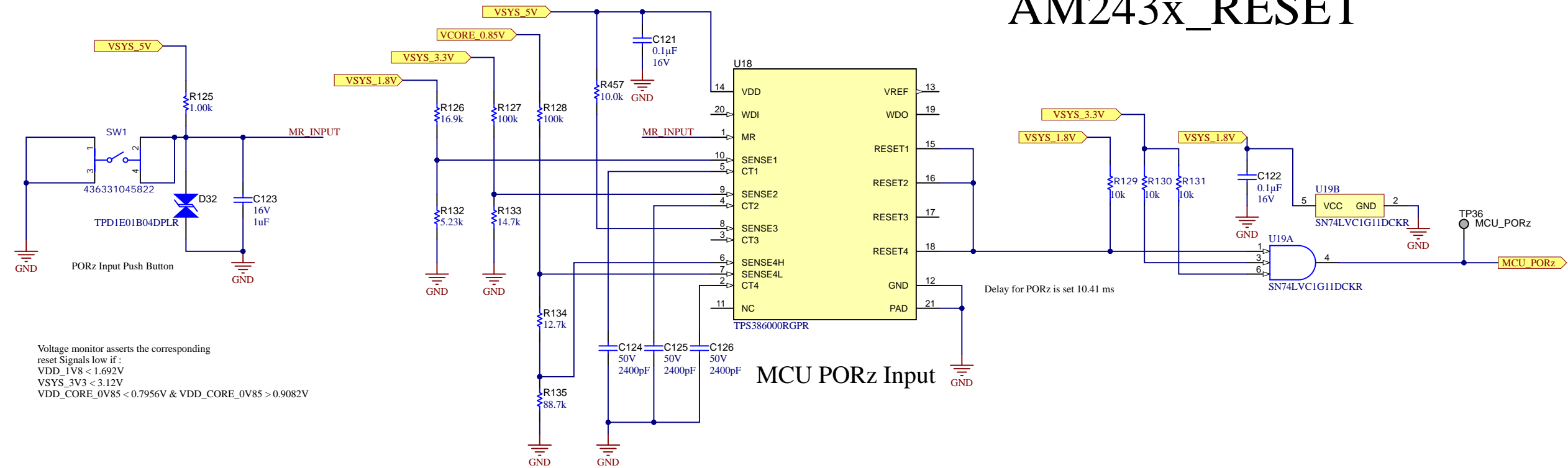
OSPI FLASH



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TID #: TIDA-010256	Project Title: HV transmit+mux frontend for ultrasound	
Number: TIDA-010256	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 7 of 18
Drawn By:	File: 15_AM243x_BootMode.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	

AM243x_RESET

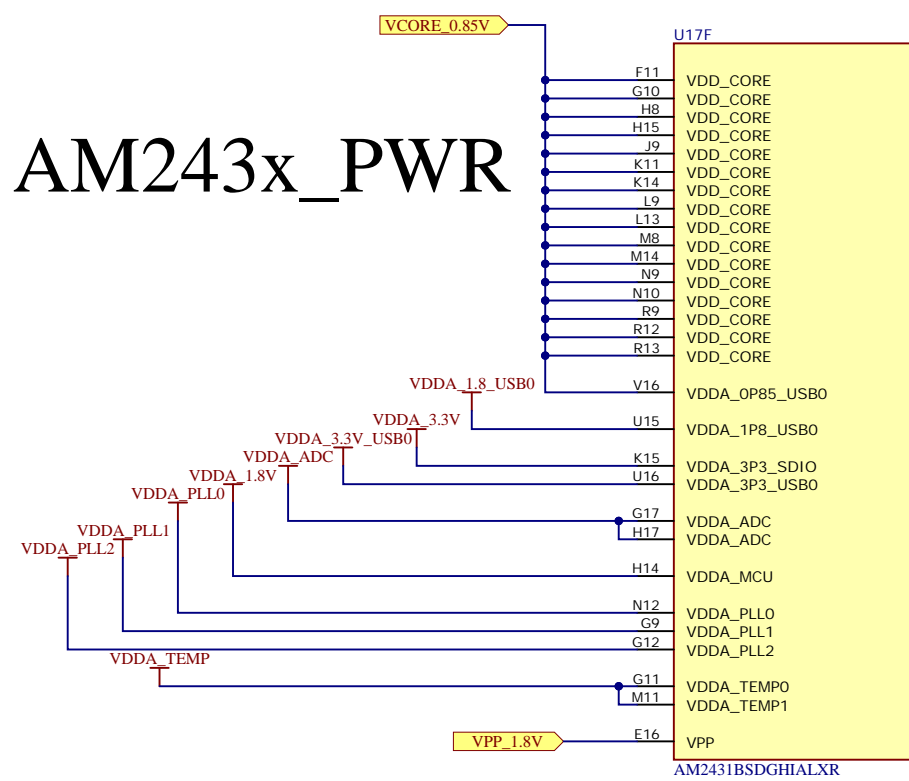


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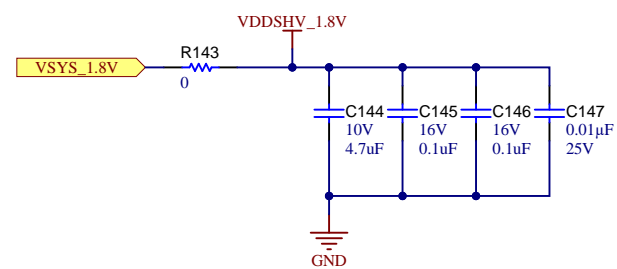
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TID #: TIDA-010256	Project Title: HV transmit+mux frontend for ultrasound	
Number: TIDA-010256	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 8 of 18
Drawn By:	File: 16_AM243x_Reset.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	

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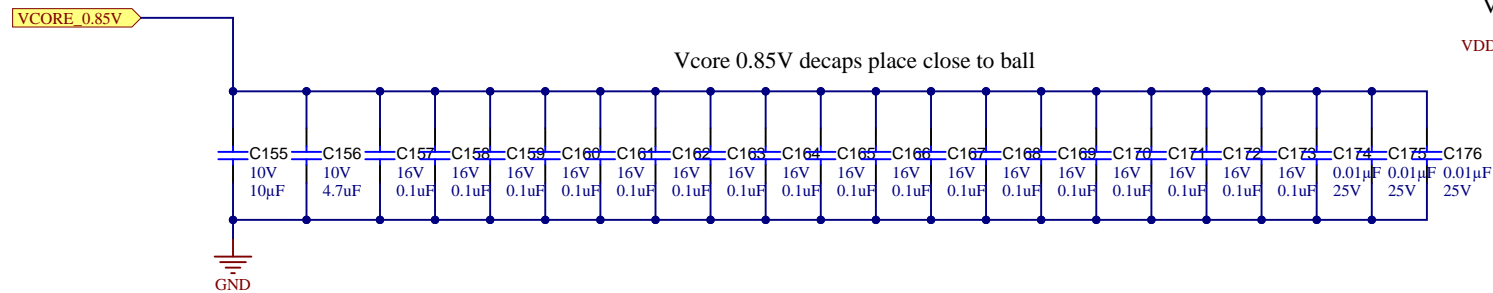
AM243x_PWR



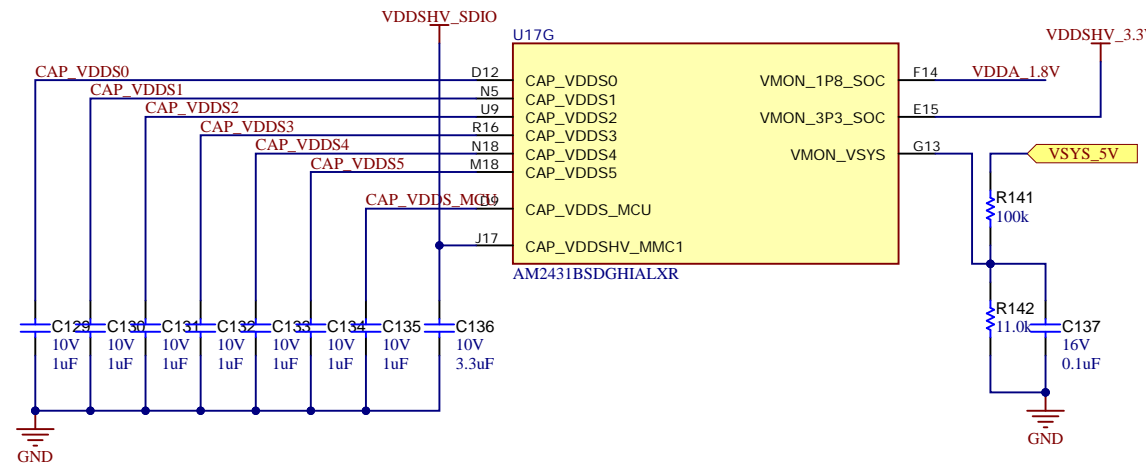
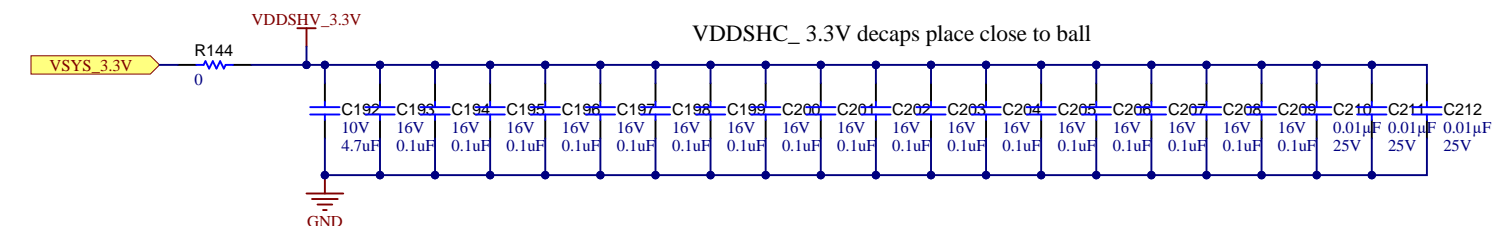
VDDSHC_ 1.8V decaps



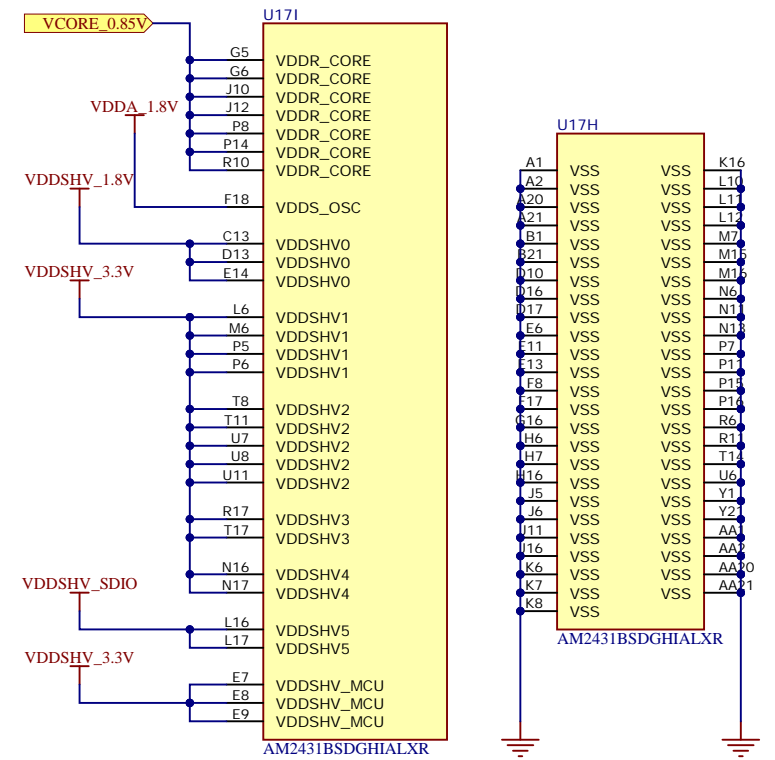
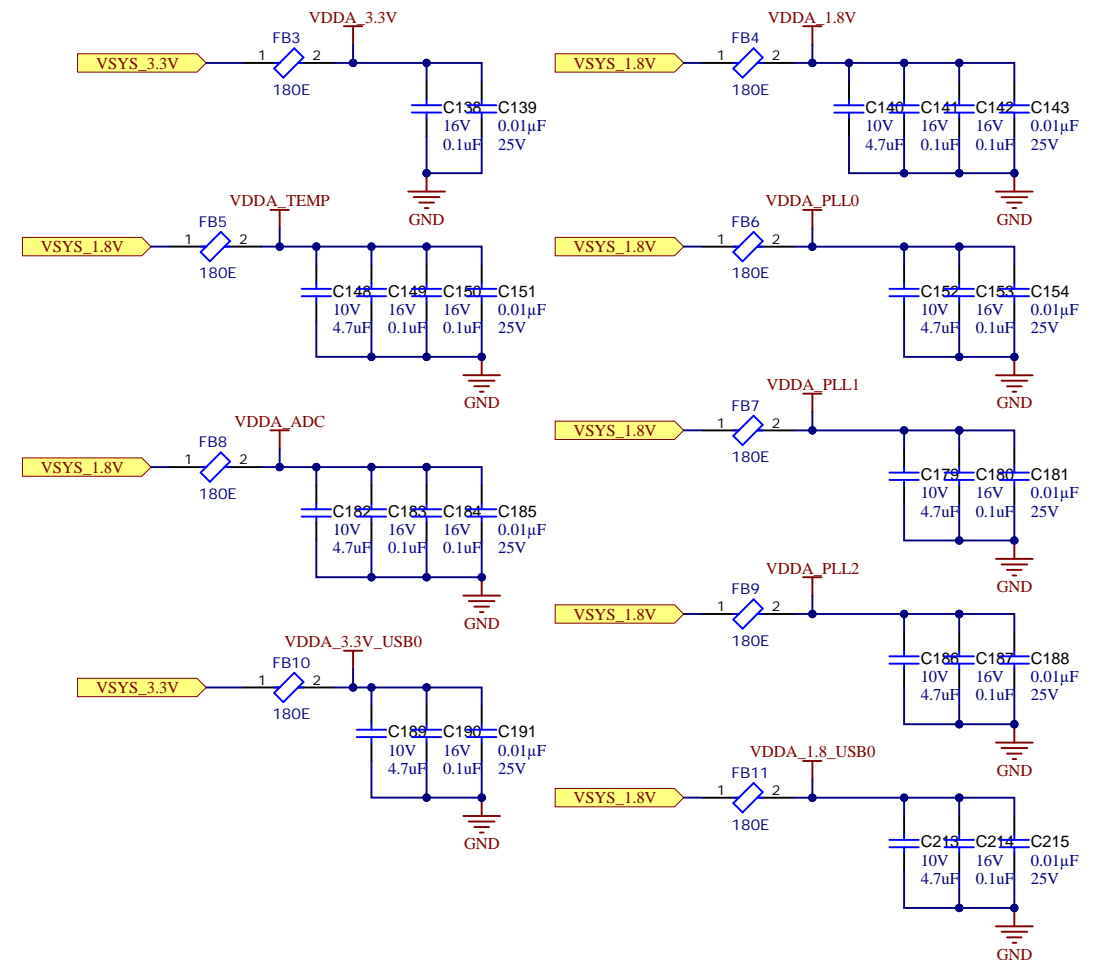
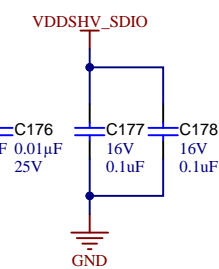
Vcore 0.85V decaps place close to ball



VDDSHC_ 3.3V decaps place close to ball




VDDSHV_SDIO decaps

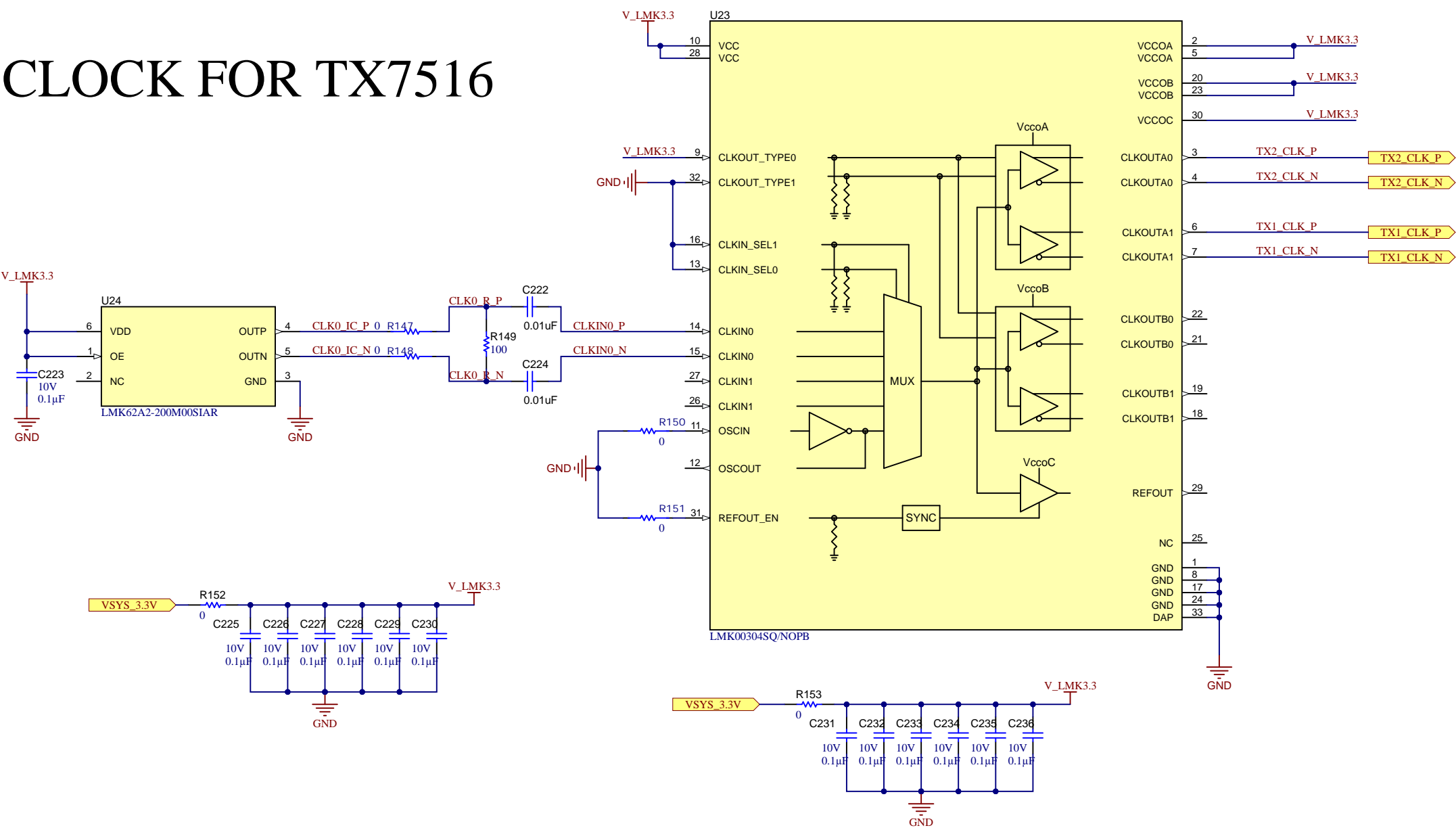


Use SPI0 communication with TX7516
VDDSHV0 connect to 1.8V

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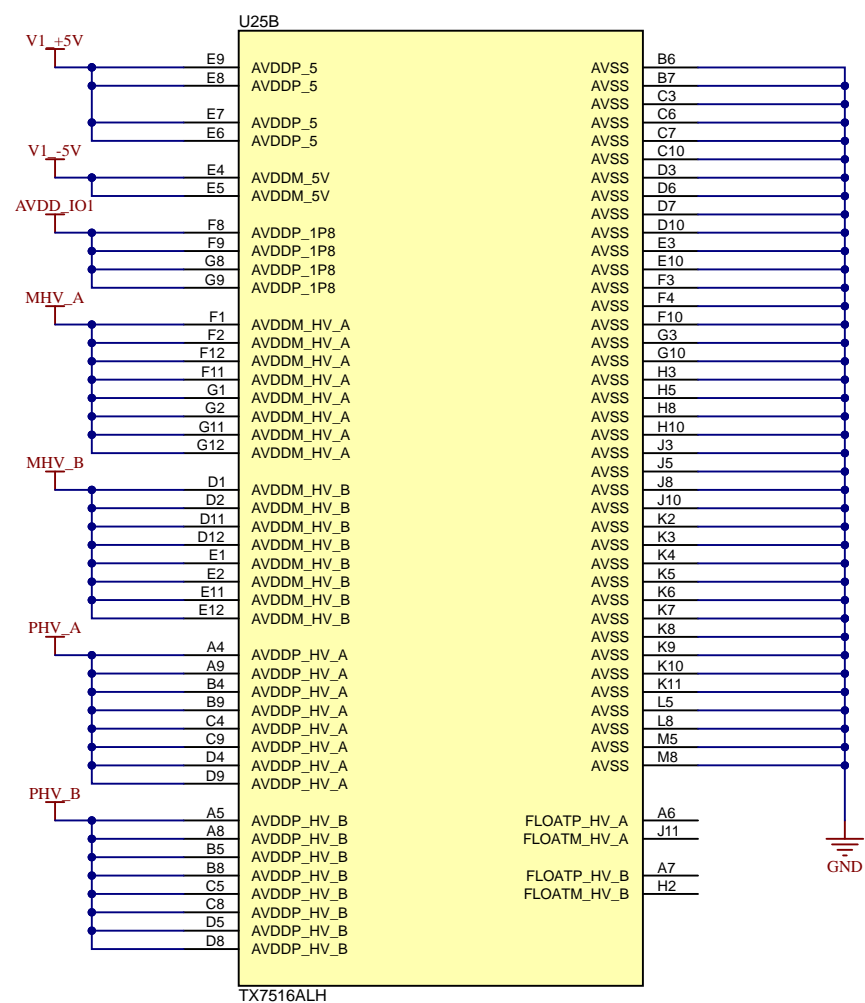
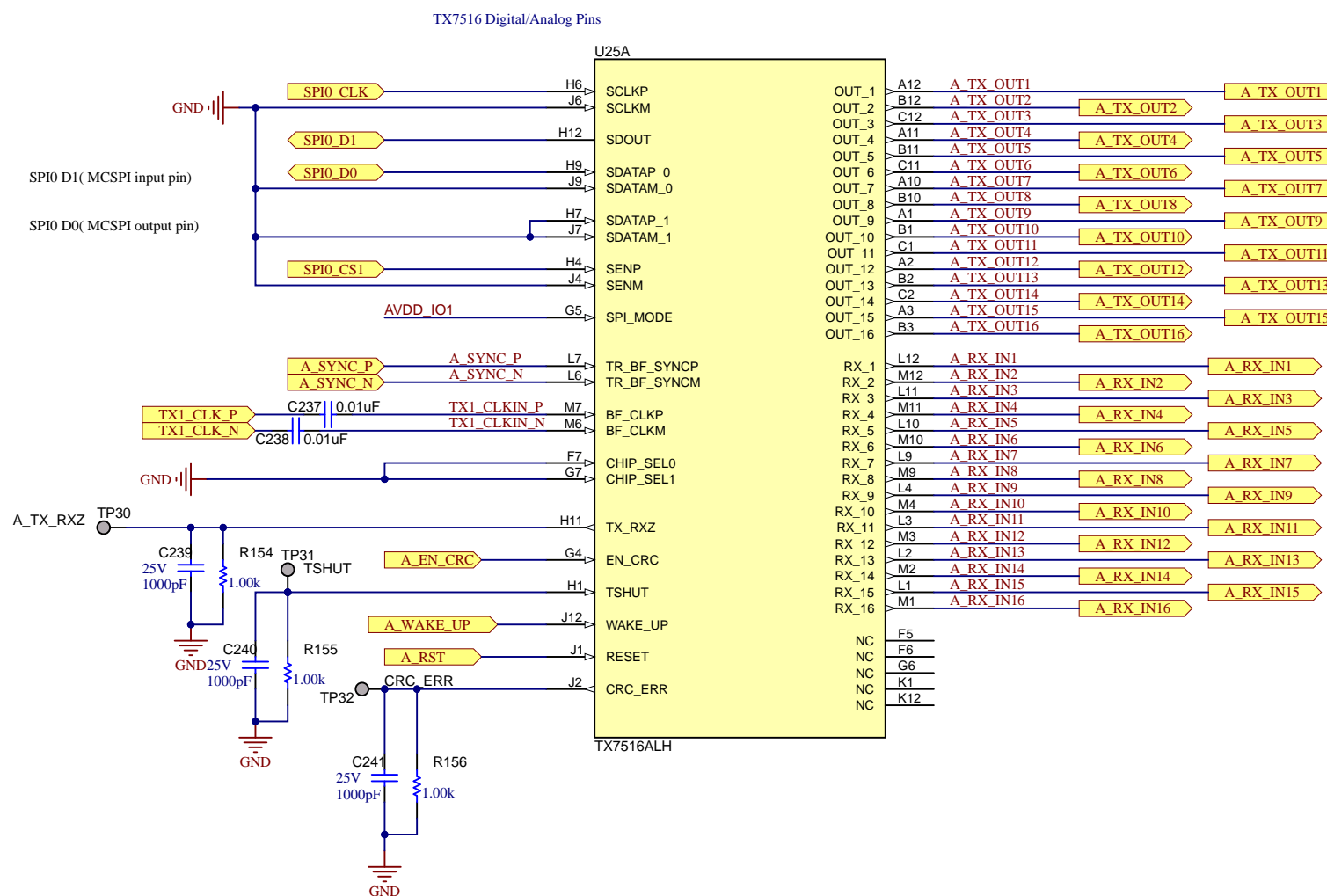
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TID #: TIDA-010256	Project Title: HV transmit-mux frontend for ultrasound		
Number: TIDA-010256 Rev: E1	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 9 of 18	
Drawn By:	File: 17_AM243x_SocPower_SchDoc	Size: B	
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2023

CLOCK FOR TX7516

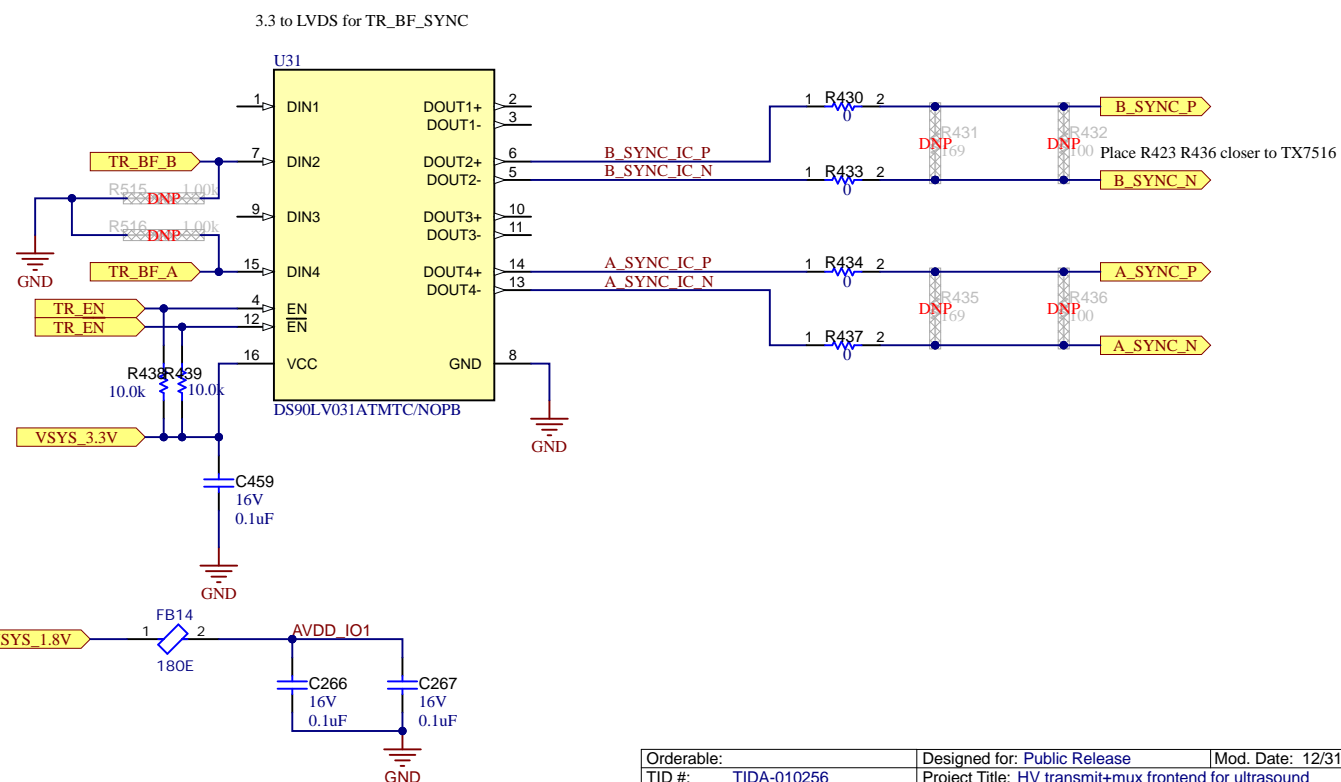
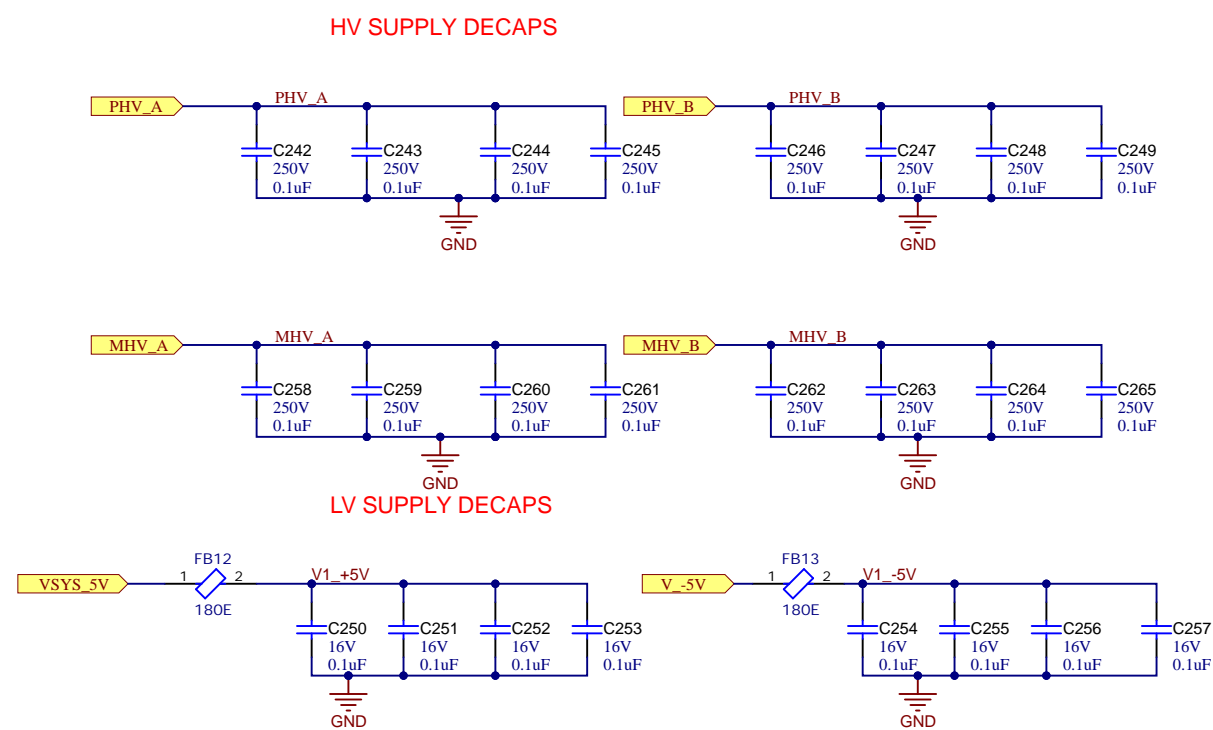


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Number: TIDA-010256	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 11 of 18
Drawn By:	File: 19_Clock_Gen.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	



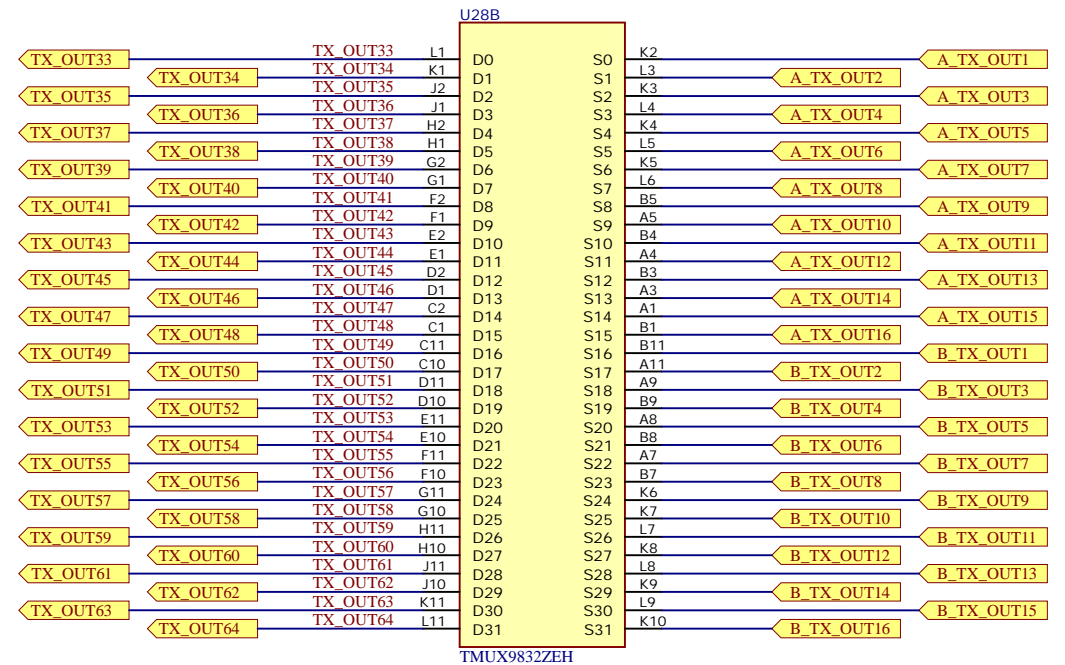
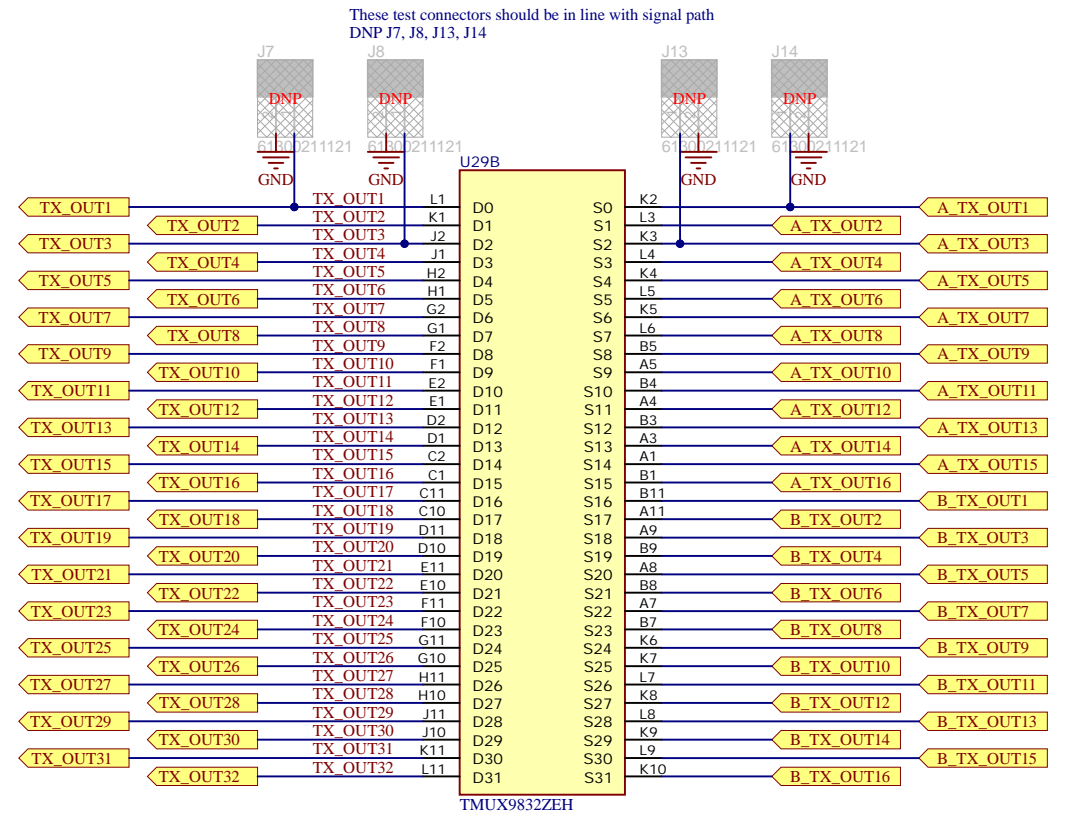
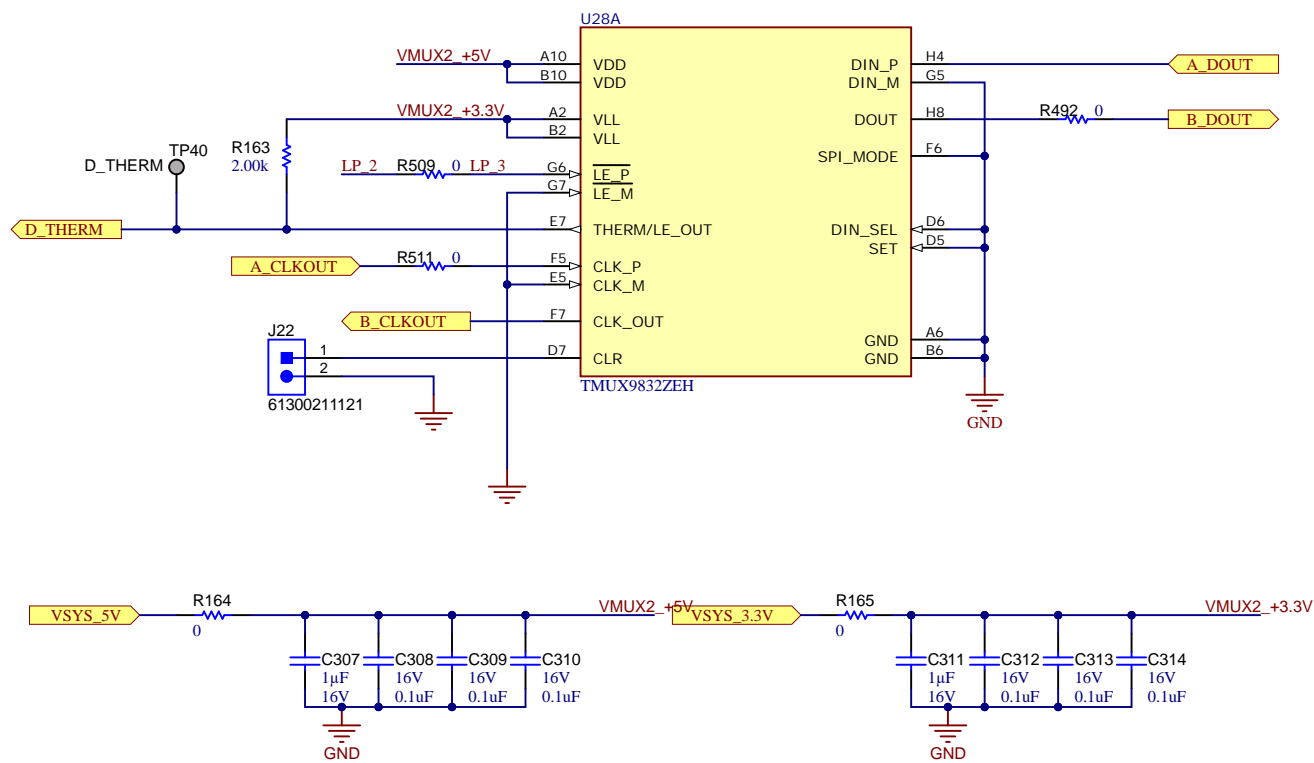
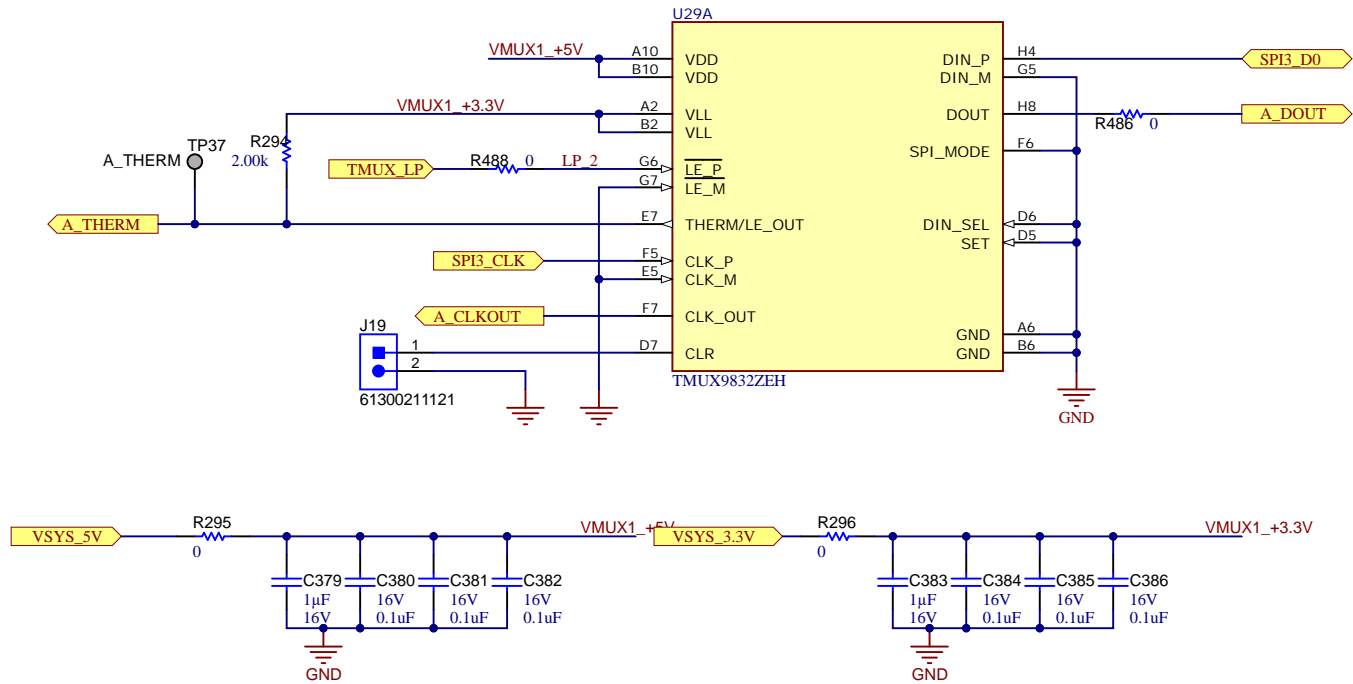
Pins C3,D3,E3,F3,G3,H3,J3,K3, K2, C10,D10,E10,F10,G10,H10,J10,K10, K11 are AVSS_DAMP ground.
Refer to layout guidelines for AVSS_DAMP routing.



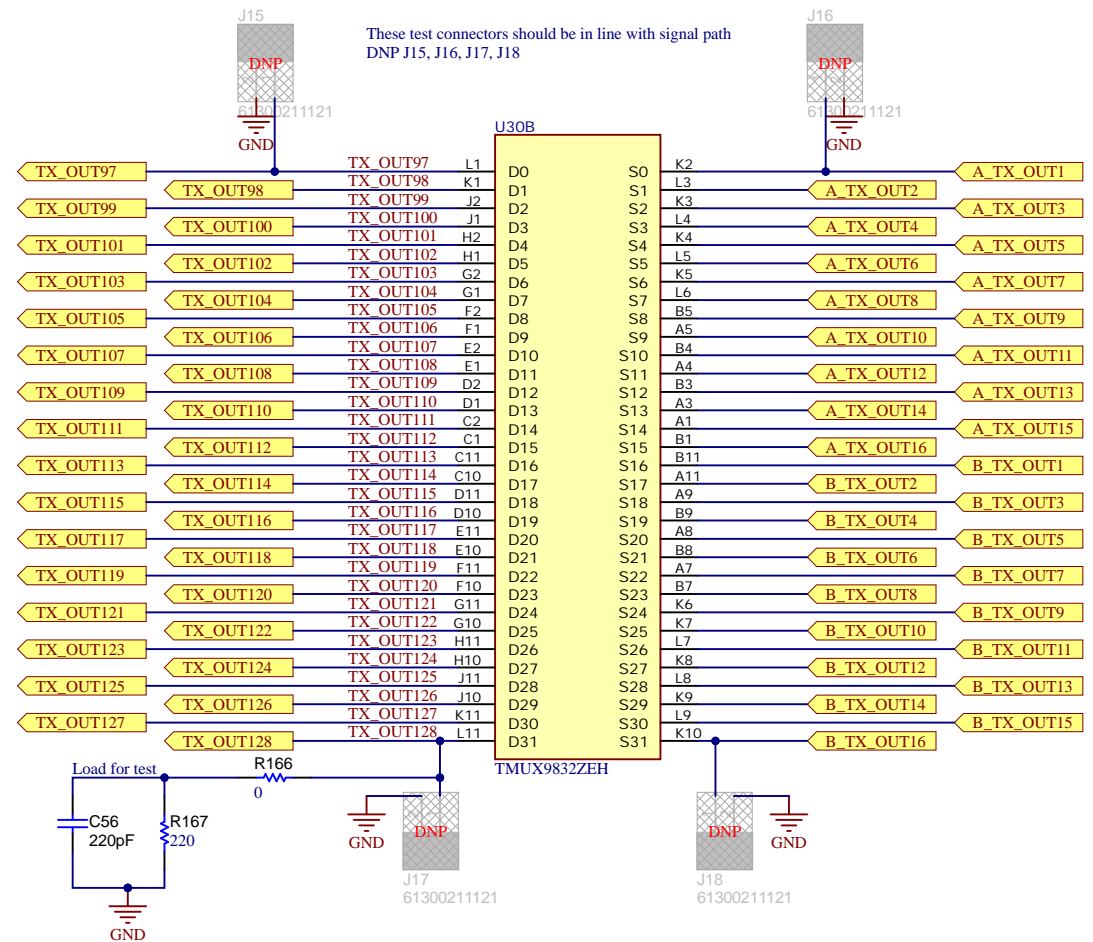
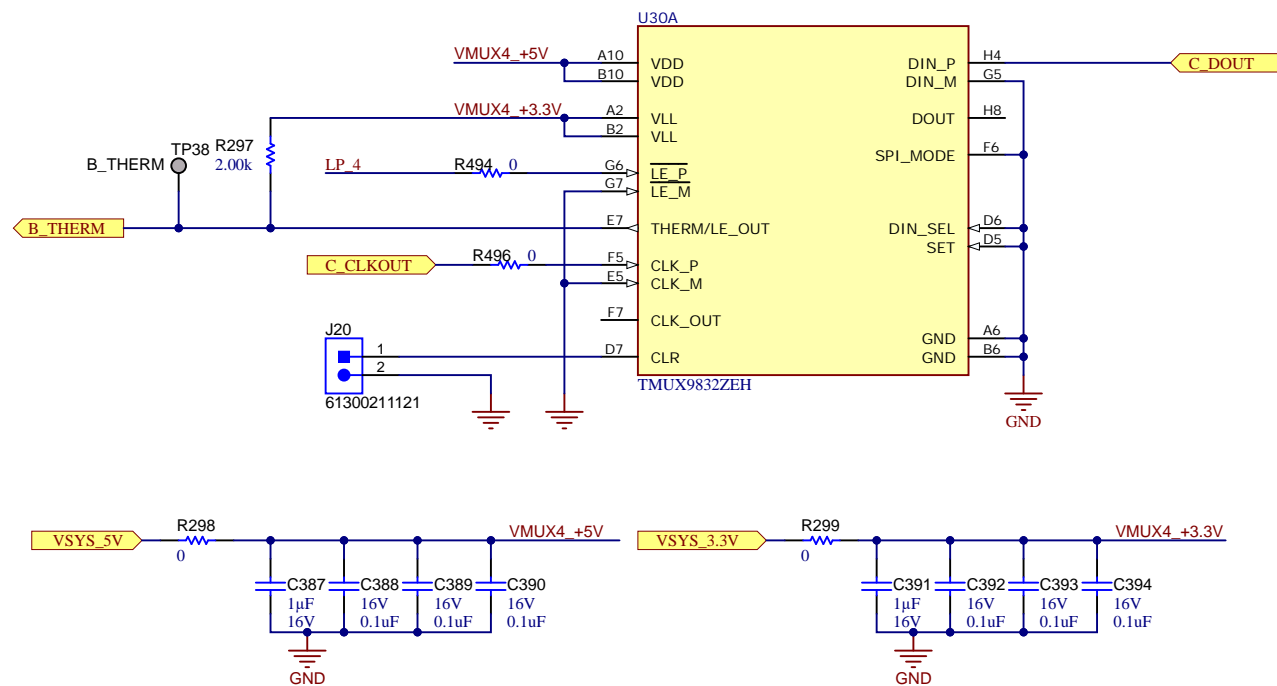
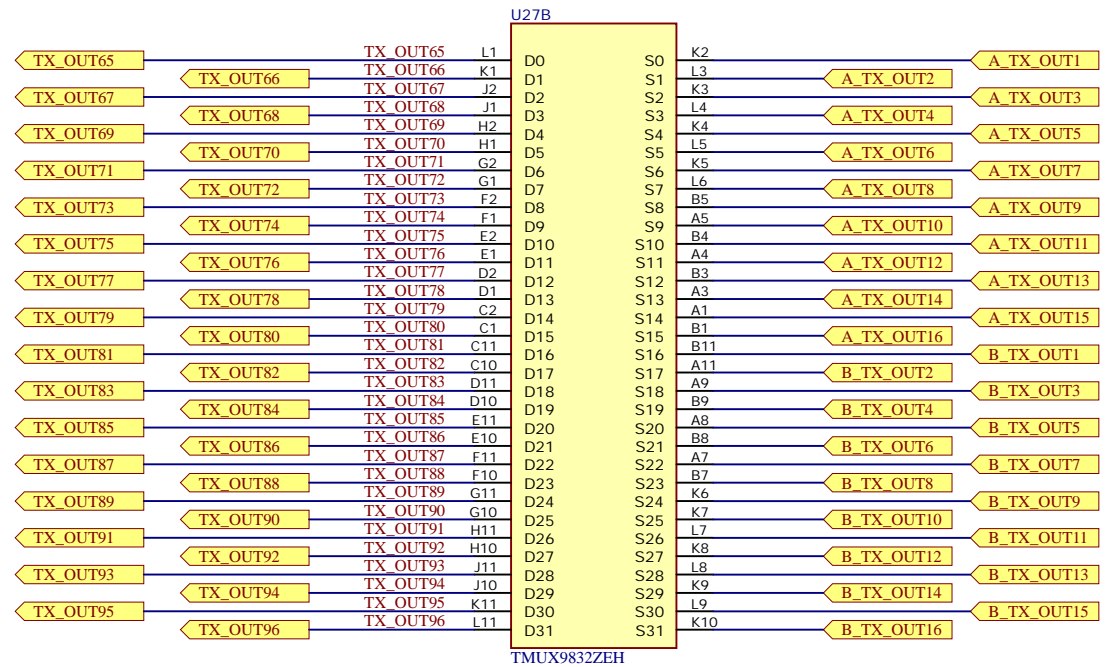
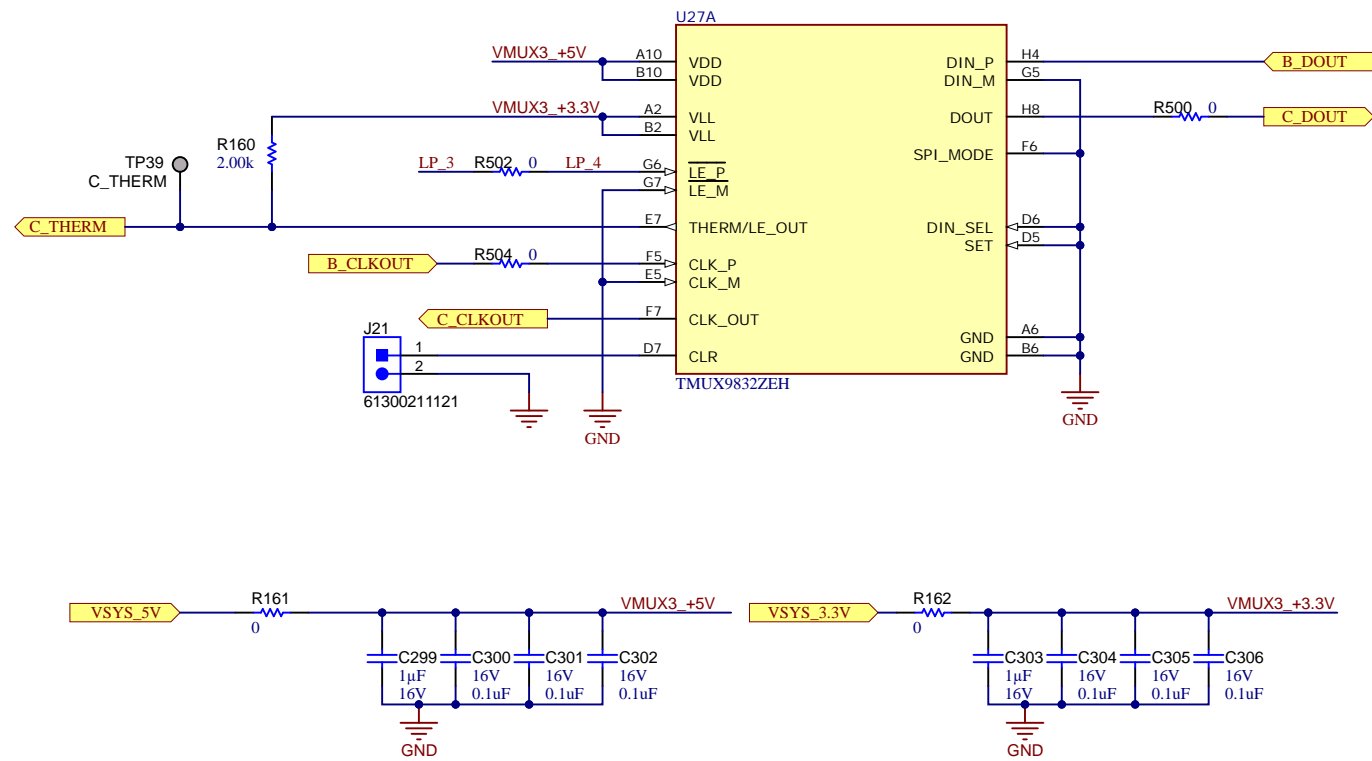
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Number: TIDA-010256 Rev: E1	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 12 of 18
Drawn By:	File: 20_TX7516_PartAPinout_SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: www.ti.com/support	

Use GPIO to control LEs
OE use for debugging daisy-chain mode and parallel mode



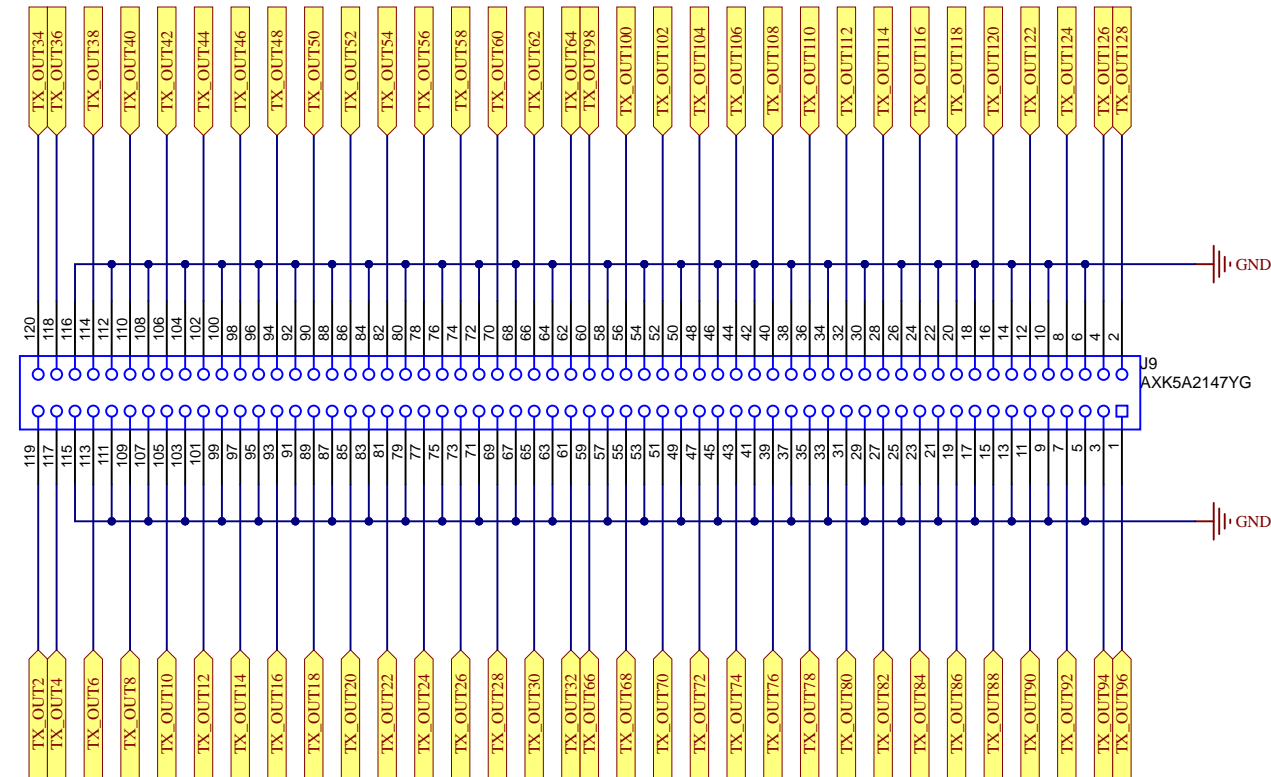
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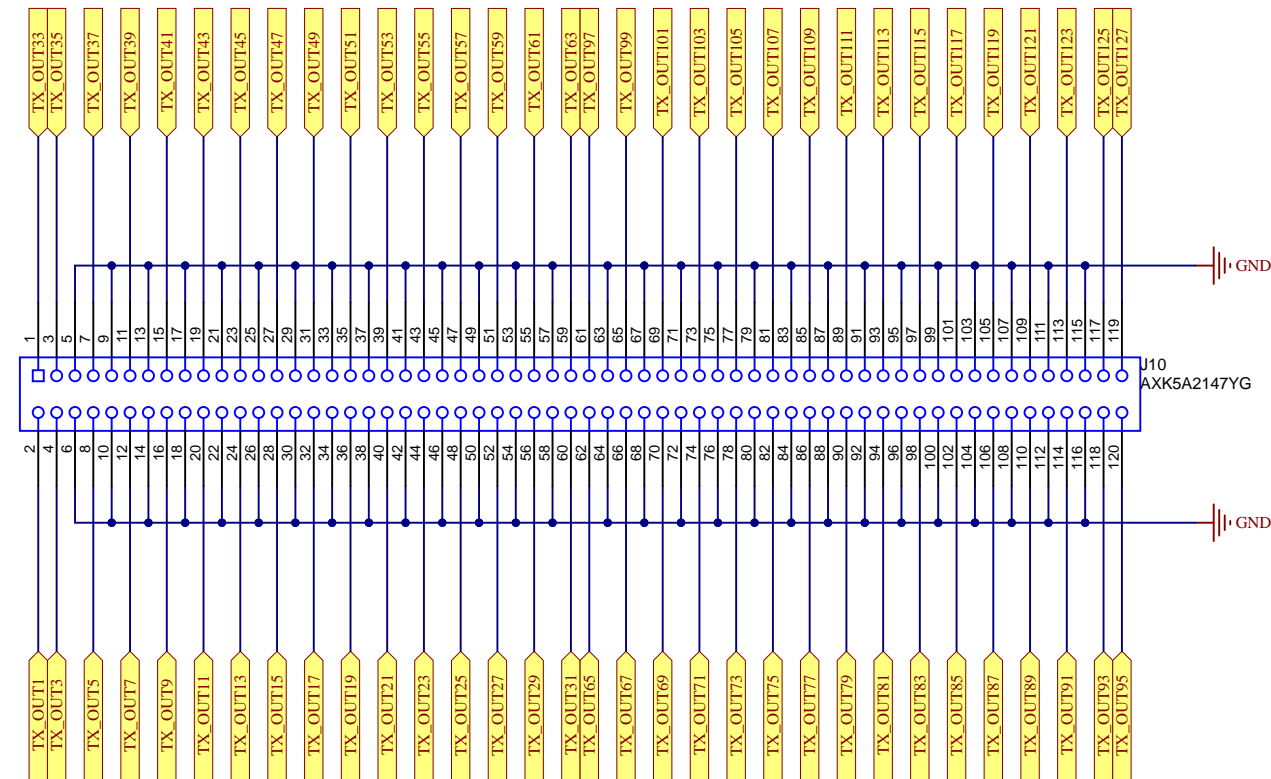
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TX CONNECTOR


Top

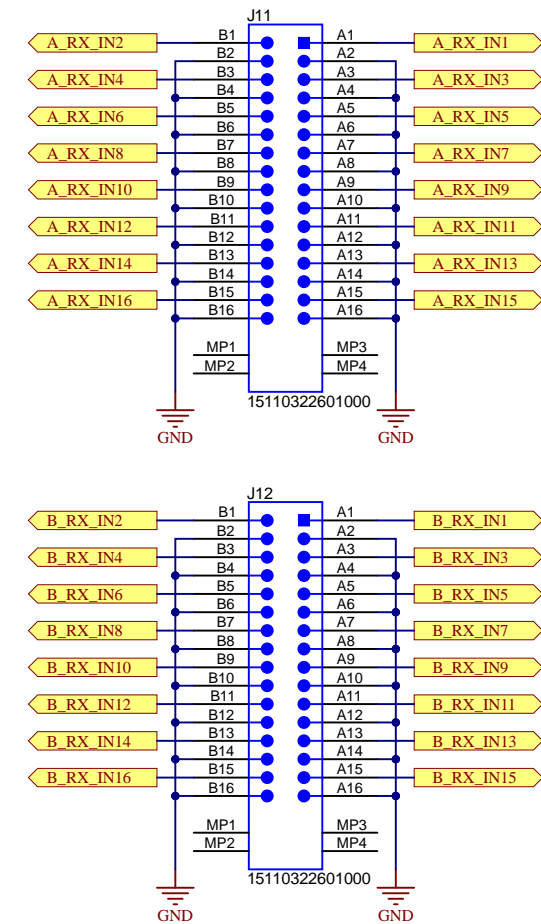
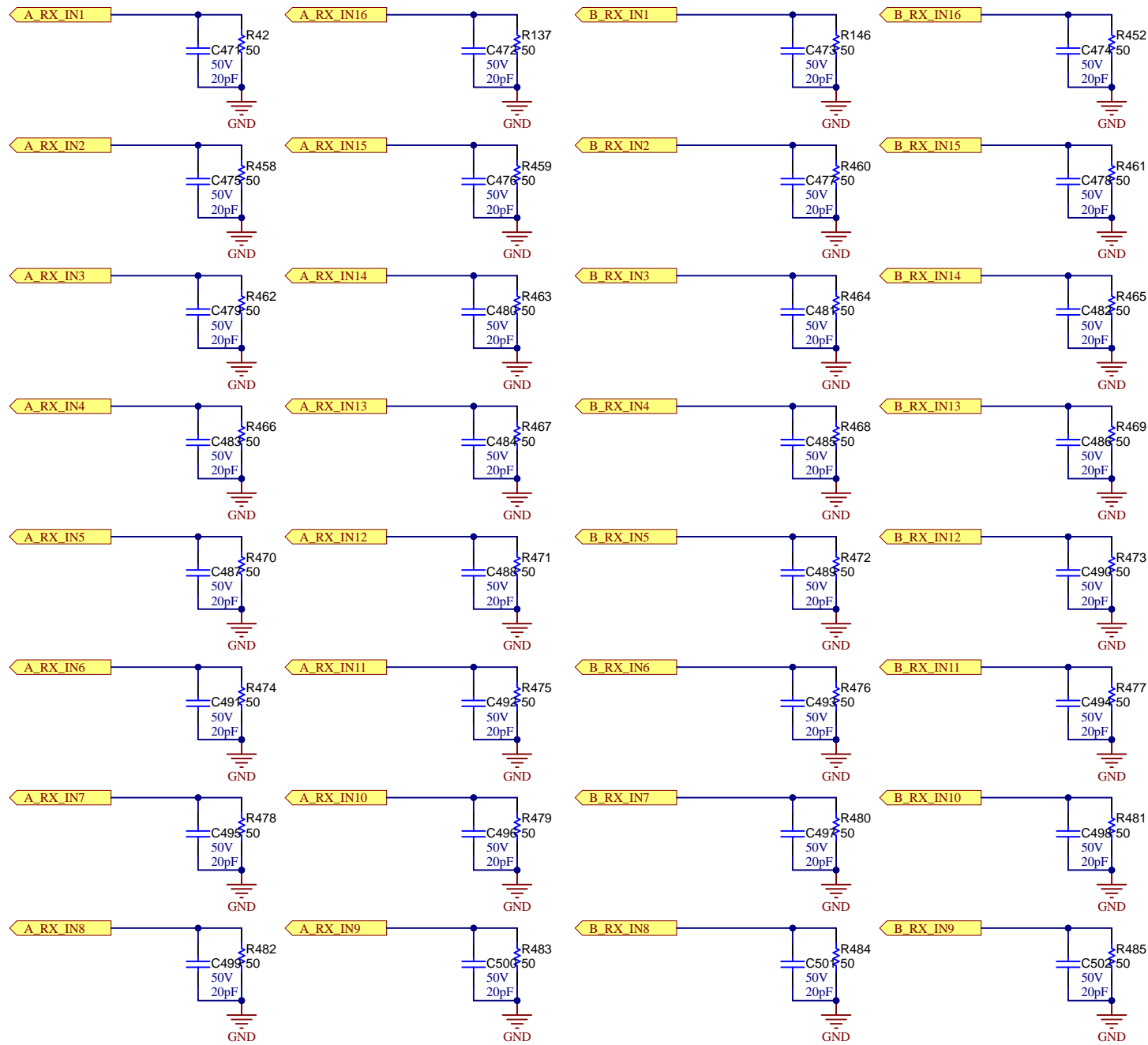


Bottom



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Number: TIDA-010256 Rev: E1	Sheet Title:		
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 16 of 18	
Drawn By:	File: 24_TX_Connector(no load resistor).SchDoc	Size: B	
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2023



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Number: TIDA-010256	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: DNP test headers	Sheet: 17 of 18
Drawn By:	File: 25_RX_Connector.SchDoc	Size: B
Engineer: Sanjay Pithadia	Contact: http://www.ti.com/support	

